

IN THE U. S. PATENT AND TRADEMARK OFFICE

In re application of

Yasuhiko KASAMA et al.

Conf. 9839

Application No. 10/577,526

Group 2811

Filed: March 16, 2007

Examiner Trang Q. Tran

Title: COMPLEMENTARY MISFET AND INTEGRATED CIRCUIT

AMENDMENT

Assistant Commissioner for Patents

May 21, 2009

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed March 13, 2009,
please amend the above-identified application as follows:

Remarks begin on page 2 of this paper.